



A two stage DC-DC converter for a Solar Photovoltaic Power Conditioning Unit

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Abstract—In this work, the design and development of a DC -DC Converter using a two stage buck–boost operation is discussed. This converter is suitable for low power application such as a matrix LED lighting system. The advantage of this converter is that instead of using two controlled switches as in case of a conventional converter, this circuit uses one MOSFET as a switch. The circuit uses two inductors and two capacitors as energy transferring elements that are able to provide a good quality, high efficiency power supply to the load. In this paper the design of TSBB converter is analyzed with closed loop control using a PI controller circuit . Even with smaller values of inductor and capacitor a highly efficient power supply can be made. The main feature of this circuit compared to the other circuits proposed is, comparing with the circuits designed previously with the same concept is that, in this work a galvanic isolation between the load and the source is provided. Thus by a suitable controller and galvanic isolation the ripple magnitude in the output is reduced considerably. A simulation model of the TSBB converter with results is presented

Index Terms—Two Stage buck–boost (TSBB) , LED Matrix, Duty cycle (α), galvanic isolation, , discontinuous conduction mode (DCM), continuous conduction mode (CCM)

I. INTRODUCTION

Energy efficient lighting is the need of the day and is becoming an area of continuous research. Illumination engineers and architects require an efficient and aesthetic lighting. In this way the LED lighting system using Matrix type of LED arrangement is becoming more popular. But the problem with such type of lighting is the power supply. Even the recent types of power supplies used for led illumination system are poor in efficiency and they are unreliable. The tungsten lamps provide only 8-10 lm/ W and very inefficient, they are then replaced by fluorescent lamps now with the efficacies of an LED is as high as 100 lm/W[3] they are mostly preferred. The problem with the LED lighting is that they cannot be supplied directly from the DC or ac voltage. Therefore there should be some kind a device to regulate the power fed to the lighting system all these mean a power supply should be developed to act as a driver circuit to the LED matrix lamp[6]. With the double buck boost converter, two converters are cascaded with a single switch and it includes two low value inductors and capacitors and suitably placed diodes thus the LED lamp can be supplied with low current low ripple and high efficiency power. In Section II, the working of TSBB converter [1] is presented. In Section III, the analysis and design of the TSBB converter [1] in various modes is detailed. In Section IV, Simulation results of TSBB converter is discussed. Section V discusses on the

laboratory prototype made and in the section VI results and conclusions are discussed.

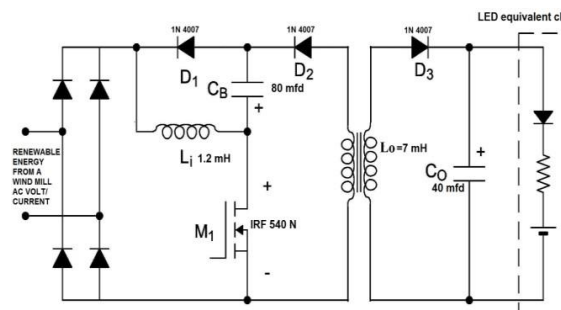


Fig. 1 Schematic circuit of a TSBB converter with isolation

II. TWO STAGE BUCK BOOST CONVERTER

Fig. 1 shows the schematic circuit of the TSBB converter. The converter acts as two buck–boost converters which are electrically isolated and connected to the load. The input buck–boost converter is made up by L_i , D_1 , C_B , and M_1 , and the output buck–boost converter here acts as a fly wheel converter due to addition of galvanic isolation and comprises of L_O , D_3 , C_O . The first converter produces a reverse output across the capacitor C_B which is corrected by the second converter and a positive output voltage with respect to ground, thereby simplifying the measurement of the load



current, in the closed-loop operation, also reducing the sensing circuitry and hence the cost.

By operating the input inductor L_i in discontinuous conduction mode (DCM), the average current through the line will be almost in phase with the line voltage, which results in a line power factor near-unity, this is important if the converter operates from mains voltage in a grid. Also, the output inductance L_o can be operated either in continuous conduction mode (CCM) or Discontinuous Conduction Mode. DCM results in a bus voltage across CB that is independent of duty cycle and output power. But there is a drawback in above said DCM since it requires a higher value of capacitance to be connected across the output in order to achieve reduced current ripple in the output of the TSBB converter. So to avoid high current ripple in the output and to reduce the value of the output capacitance, CCM operation is preferred. In addition to above, to reduce the ripple voltage occurring at low frequencies, the second stage of the TSBB is operated with $\alpha = 50\%$ (duty cycle), since the duty cycle is multiplied by the buck-boost converter voltage ratio. In this way, it will be possible even to use a film capacitor to implement the output capacitance; this gives the TSBB converter longer life rating and improved efficiency over the use of electrolytic capacitors.

Moreover, with a careful design of the converter, the bus capacitor can also be made low enough to be implemented with film technology, thus avoiding the low-life-rating electrolytic capacitors in the whole converter. This implies the design of the converter so that it operates with a α lower than 0.5. In this manner, the output converter voltage ratio will be lower than one, thus reducing the low-frequency voltage ripple in the same amount

The equivalent circuits for the operation of the TSBB converter for one cycle a switching period are shown in Fig. 2.

A. Need for Isolation

Among the regulators non-isolated switching regulators are very common, or Point of Load (POL) converters. Although low cost and simple, these converters suffer from one disadvantage in that there is an electrical connection between the input and output. Many safety agency bodies require a separation from the applied input voltage and the output voltage which is often user accessible. An isolated DC-DC converter will have a high frequency transformer providing that barrier. This barrier can withstand anything from a few hundred volts to several thousand volts, as is required for medical application. A second advantage of an isolated converter is that the output can be configured to be either positive or negative. Where many users get confused concerns how to connect the input up, particularly with the differences between a data com system where the input

negative is connected to chassis and a telecom system input positive is connected to chassis.

III. ANALYSIS OF TSBB CONVERTER

In this section, the TSBB converter is analyzed when operated from the voltage supplied by a renewable energy source namely a windmill for, achieving a low-ripple current through the power-LED load. For the sake of the analysis it is assumed that the line voltage is a perfectly sinusoidal whereas in case of a real wind mill the output sine wave may have voltage sag and swell. The input voltage is thus given as

$$v_p(t) = V_m \sin \omega t.$$

A. Line Current

The input current i_p corresponds to the current through the inductance L_i during the time interval $0 - \alpha T_s$, where α is the transistor duty cycle and T_s is the transistor switching period. This current is modulated by the rectified line voltage [1], as. Thus, the value of the input current averaged at line frequency can be calculated as follows [1]

$$\frac{\alpha^2 V_p}{4L_i f_s}$$

$$i_p = 2L_i f_s \sin \omega t \quad \dots 1$$

where i_{p_peak} is the instantaneous peak current in each switching period, f_s is the switching frequency, V_p is the peak line voltage, and ω is the line angular frequency. The mean input power P_i can now be calculated as [1]

$$P_i = \frac{\alpha^2 V_p^2}{4L_i f_s} \quad \dots 2$$

B. Output and Bus Voltages

The output voltage V_o for the ideal converter can be obtained by equalling input and output powers. The output power is obtained as follows:

$$P_o = \frac{V_o^2}{R} \quad \dots 3$$

with R being the static equivalent resistance[5] of the LED load, which can be obtained by the ratio between the dc values of LED voltage (V_{LED}) and current (I_{LED}) at each operating point[7]

$$R = \frac{V_y}{I_{LED} + R_y} \quad \dots 4$$



where V_γ and R_γ are the voltage and resistance parameters of the LED lamp equivalent circuit [8] Then, assuming 100% efficiency, by equalling (2) and (3), the output voltage is finally obtained

$$V_o = \frac{\alpha V_p}{2 - D} \quad \dots 5$$

Where D is a non-dimensional factor given by

$$D = \frac{f_s L_i}{R} \quad \dots 6$$

Since the output stage corresponds to a buck–boost converter (flywheel converter) operating in CCM, the bus voltage V_B can be calculated by using the voltage conversion ratio for this converter [2]

$$\frac{1 - \alpha}{\alpha} V_o = \frac{1 - \alpha}{2} \frac{V_g}{D} \quad \dots 7$$

As can be seen from (5) and (7), when operating the input stage in DCM and the output stage in CCM, the bus and output voltages are reversely dependent on the duty cycle α . For example, if the α increases, the output voltage increases, and the bus voltage decreases in the same amount. The sum of both voltages does not depend on the duty cycle, being only proportional to the line peak voltage, as given by the following:

$$V_B + V_o = \frac{V_g}{2 D}$$

C. Energy transferring components

The value of the energy transferring element at the input which is L_i is calculated based on the output power and assuming 100% efficiency the value of inductance is so obtained to be

$$L_i = \frac{\alpha^2 V_p^2}{4 P_o f_s} \quad \dots 10$$

Similarly the value of the capacitor which is another energy transferring element called as bus capacitor C_B is calculated so as to limit the low-frequency ripple of the bus voltage, this bus voltage is what applied to the second stage of the TSBB converter. The current through this capacitor is also the currents through diodes D_1 and D_2 . In these diodes, only the current through D_1 is modulated by a rectified line

frequency. In order to calculate the bus ripple, the low frequency component of the current through D_1 must be

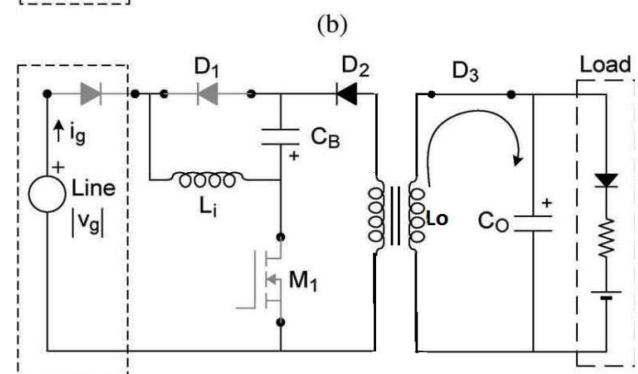
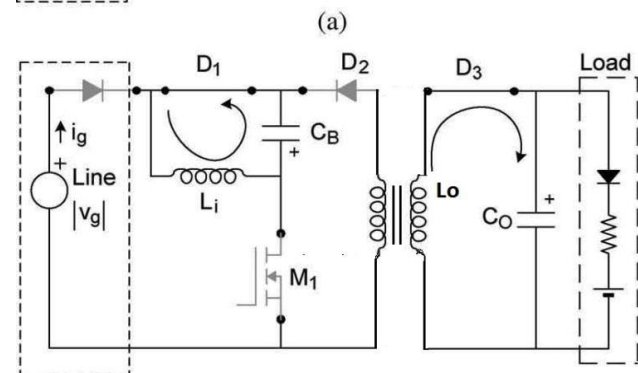
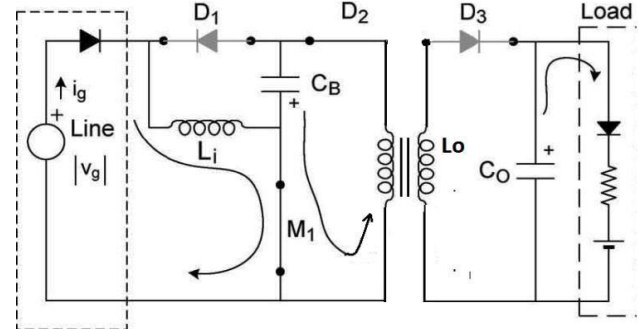


Fig. 2. Equivalent circuits for the operation of the TSBB converter. (a) Interval I: $0 < t < DTS$. (b) Interval II: $DTS < t < DTS + t1$. (c) Interval III: $DTS + t1 < t < TS$.

obtained. The average current through D_1 can be calculated as follows:

$$i_{D1} = \frac{1}{T_s} \frac{i_{D1_peak} t_1}{2} \quad \dots (11)$$

Where i_{D1_peak} is the peak current through D_1 in each switching period and t_1 is the time needed by this current to



reach zero. Both values are changing after double the line frequency and have the following values:

$$i_{D1_peak} = L_i \frac{V_p \alpha T_s}{\alpha V_p T_s} \dots(12)$$

$$t_1 = V_B \dots(13)$$

Using (12) and (13) in (11), the D1 average current is given as follows

$$i_{D1} = 2V_B f_s \alpha T_s 2 L_i L_i^2 i_{D1_peak}^2 \dots(14)$$

Equation (14) can also be expressed as follows:

$$i_{D1} = 2V_B f_s L_i \frac{\alpha^2 V_p^2}{4V_B f_s L_i} \dots(15)$$

Then, the low-frequency ac component circulating through D1 and CB is given by

$$i_{D1_ac} = \frac{\alpha^2 V_p^2}{4V_B f_s L_i} \dots(16)$$

Now, the low-frequency peak-to-peak ripple voltage across capacitor CB, ΔV_{B_LF} can be obtained as

$$\Delta V_{B_LF} = \frac{\alpha^2 V_p^2}{8\pi V_B L C f} \dots(17)$$

current through diode D1, obtained from (16), and f_L is the line frequency. The necessary bus capacitance for a given peak-to-peak ripple in the bus voltage is then calculated from

(17) as follows:

$$C_B = \frac{\alpha^2 V_g^2}{8\pi V_B L_i \Delta V_{B_LF} f_s f_L} \dots(18)$$

Note that, as long as the output buck–boost converter operates in CCM, the LED current ripple at low frequency depends only on the bus voltage ripple and, thus, on the bus capacitance CB. The output capacitance CO has no effect on this low frequency ripple. Finally, the output inductance and capacitance LO and CO are obtained using the well-known expressions for a buck–boost converter operating in CCM

$$L_O = 0.5 \frac{\Delta I_{L_O_HF} f_s}{\alpha I_O} \dots(19)$$

$$C_O = \frac{I_O}{\Delta V_{O_HF} f_s} \dots(20)$$

where $\Delta I_{L_O_HF}$ is the LO high-frequency peak-to-peak current ripple, ΔV_{O_HF} is the high-frequency peak-to-peak output voltage ripple, and I_O is the dc current through the LED load.[4]

IV. SIMULATION RESULTS

Simulation of the TSBB converter was done using PSim Software. It was found that Psim was easier to use and the results can be obtained quicker as the time taken for the simulation was less.

The circuit configuration shown in fig 3 shows, TSBB converter in closed loop mode using a PI controller. The values of the PI controller elements are so chosen to give a reduced steady state error. Instead of going in for standardised tuning methods, it was found that manual tuning gave satisfactory results. So the tuning of the PI controller was done based on trial and error method manually.

The Proportional gain (Kp) is given by the equation (21) where the values of R1 and R2 are determined by manual tuning method [9]

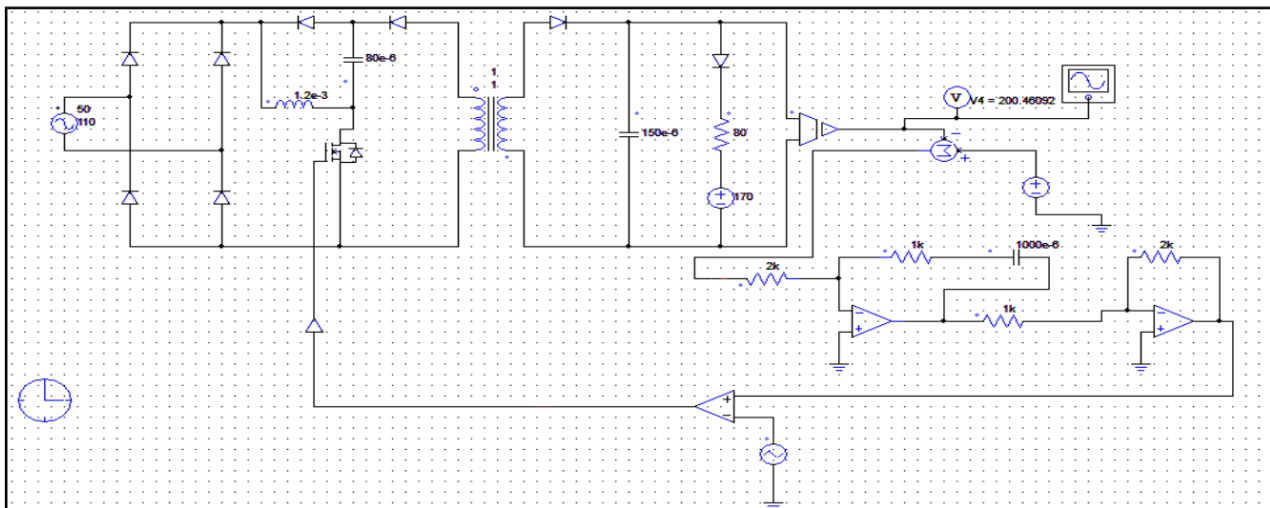


Fig. 3 Simulation circuit of the DBB converter in closed loop



$$K_p = \frac{R_2}{R_1} \quad (21)$$

Similarly, the integral gain (Ki) is given by the equation (22) where the values of R1 and R2 are determined by manual tuning method [9]

$$K_i = \frac{1}{R_2 C} \quad (22)$$

Fig 4 shows the pulse width modulated gate signals [4] applied to the gate source terminals of the MOSFET switch. The

frequency of the gate signal is maintained at 5 kHz with an α greater than 50%. It can be noted from the output taken across the load as shown in Fig 5, which is the equivalent circuit of LED matrix, that when isolation is provided, there is drastic drop in the magnitude of voltage ripple a value of less than 3 Volts. Thus this converter provides a good regulation of output voltage and is suitable for dc load application involving either renewable energy or from mains power supply. Thus the two stage buck-boost converter was simulated using Psim software and the results were obtained as per the requirement. The output was high compared with conventional buck-boost converters with 2 switches [9].



Fig4 .screen shot of gate pulses applied to gate-source terminal of the MOSFET switch

V. LABORATORY SETUP

A laboratory prototype was setup using electronic components on bread board and through the use of regulated power supplies, function generator and a 30Mhz oscilloscope. The load was a low power LED matrix lamp[3] of 20 V operating voltage and 36 Watts power [2].

The entire laboratory setup is shown in fig. 6 connected to various sources and sinks. In fig 7 the DBB converter assembled on a bread board can be seen . the inductors used

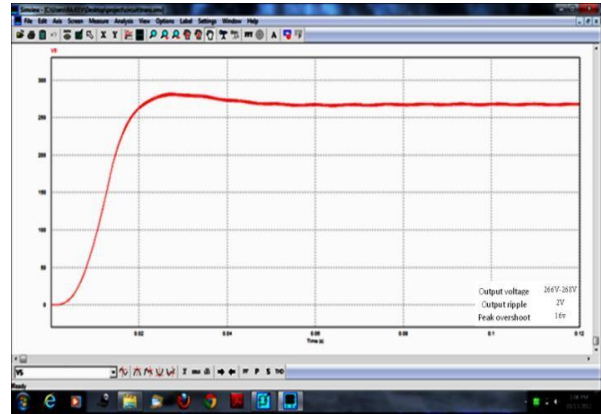


Fig 5 Simulation circuit for output voltage with galvanic isolation in this work is hand wound inductors as per the design given below

Gauge of copper wire used : 32 SWG
Diameter of the copper wire : 0.274mm

Ratings of Inductors
 $L_i = 1.2$ milli Henry $L_o = 7.0$ milli Henry
 $N_1 = 47$ turns $N_2 = 163$ turns

As discussed in the equations the value of the bus capacitor CB and output capacitor Co are of the values mentioned below

$$C_B = 80 \mu F \quad C_o = 40 \mu F$$

Fig 8 shows the control section of the TSBB converter assembled on a bread board. It mainly consists of a PI controller which is manually tuned as per the values shown in the Fig.3. The output of the PI controller which is negative is given to the inverter, the output of the inverter a, DC line is now compared with high frequency triangular



Fig. 6. Complete laboratory prototype setup

carrier wave to produce PWM pulses (seen in fig 9) which is applied to the MOSFET switch .The output voltage which is a ripple free, boosted and controlled DC is shown in the CRO as depicted in the fig. 9.

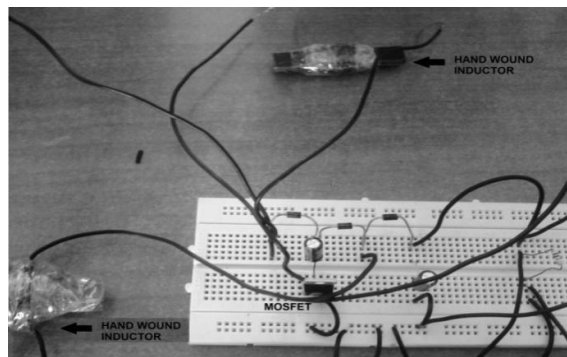


Fig 7. DBB converter on bread board with hand wound inductors.

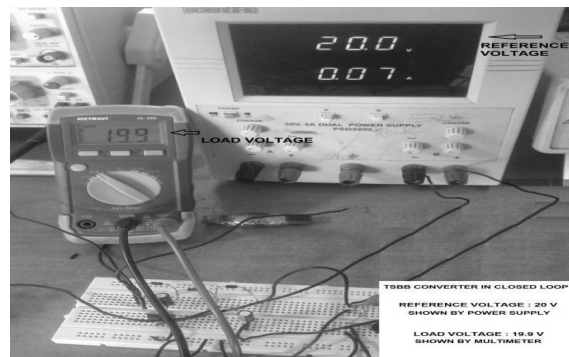


Fig. 10 reference voltage and the output voltage displayed

The CRO is functioning in dual channel mode with one channel showing the output voltage and the other channel showing the Pulses applied to the gate source terminals of the MOSFET. Fig.10. shows the output voltage and the reference voltage. The reference value of 20 V is set up using a Regulated power supply. The output across the load is measured using a multi-meter to read a value of 19.9 V as same as that of the reference. Thus the output voltage obtained is controlled and as per the reference.

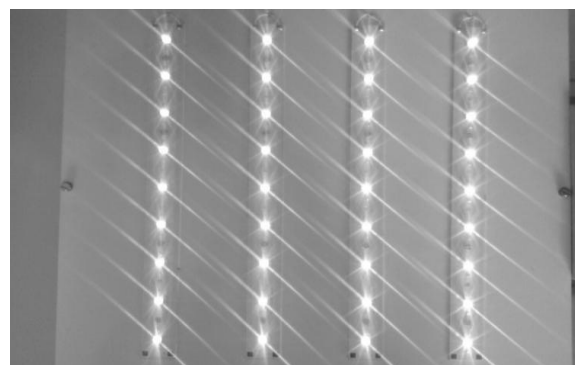


Fig. 11. 36 W LED matrix working from TSBB converter

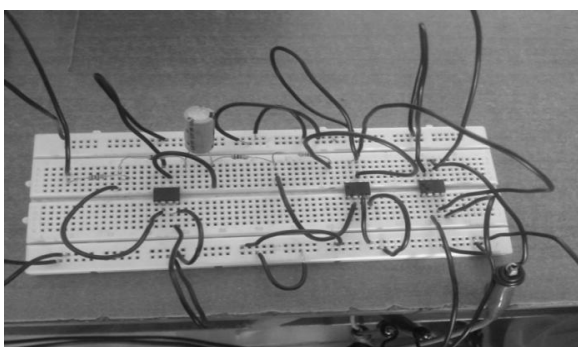


Fig 8 PI controller with inverter and PWM generator

The output which is applied to the LED matrix lamp makes it to glow brightly as shown in fig 11. Thus the working of prototype TSBB converter with LED lamp load was successfully implemented.

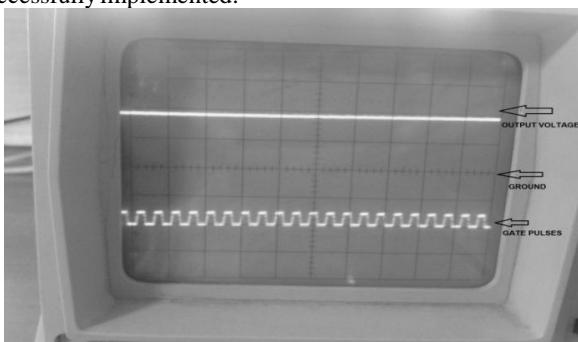


Fig 9. Gate pulses and the output voltage on the CRO

VI. CONCLUSIONS

A two stage buck boost converter (TSBB) has been successfully simulated in this paper there by a circuit configuration is discussed for providing quality power supply for low power applications In closed loop mode suitable controller namely the PI controller was used.

It is evident from the results discussed in the previous sections that the configurations of TSBB converter in closed loop is efficient compared to the converters using more than one control switch. .

The TSBB converter was implemented as a laboratory prototype using electronic components on a bread board as discussed in the previous section. It can be seen that the ripple content in the output is nil and the output voltage follows the reference voltage due to the use of PI controller.

Thus realization of a buck-boost converter using a single switch was done. The output is controlled.

The added advantage of this converter is that this is suitable for power converters used for medical applications where isolation is essential. Though Isolation may end up in increase of cost, it is a safety aspect that cannot be neglected in certain applications



Thus it can be concluded that the configuration of a TSBB converter in closed loop mode is more efficient, safe and reliable for any low power loads.

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